



DMN2041L

N-CHANNEL ENHANCEMENT MODE MOSFET

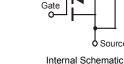
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

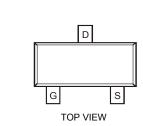
Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (approximate)





TOP VIEW



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2041L-7	SOT-23	3000/Tape & Reel

Source

Drain

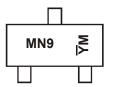
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. Notes:

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



MN9 = Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site) YM = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or \overline{Y} = Year (ex: A = 2013) M = Month (ex: 9 = September)

Chengdu A/T Site

Σ MN9

Shanghai A/T Site

Date Code Key

Year	200	9	2010		2011	20	12	2013		2014	2	2015
Code	W		Х		Y	Z	Z	А		В		С
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characte	eristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 5)Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			I _D	6.4 4.5	A
Pulsed Drain Current (Note 6)	•	·	I _{DM}	30	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.78	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C	R _{0JA}	161	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

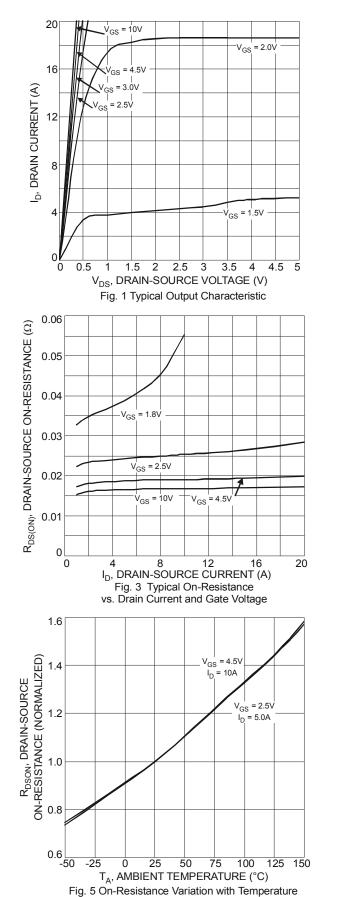
Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout. 6. Repetitive rating, pulse width limited by junction temperature.

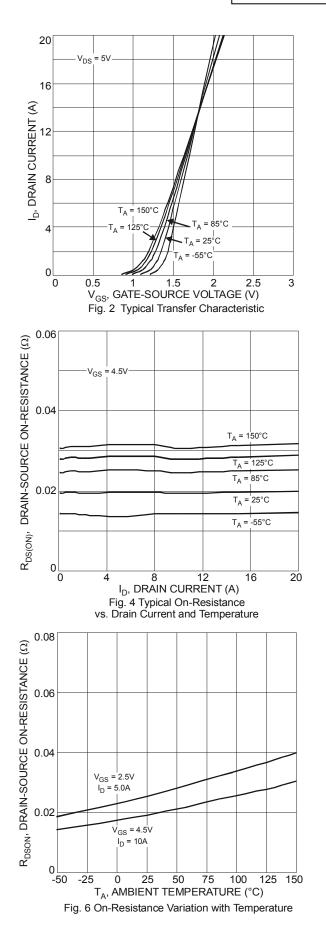
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Turp	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Symbol		Тур	IVIAX	Unit	Test condition
Drain-Source Breakdown Voltage	BV _{DSS}	20	_		V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current $T_J = 25^{\circ}C$		20		1.0	μA	$V_{\rm DS} = 20V, V_{\rm GS} = 0V$
Gate-Source Leakage	I _{DSS}			±100	nA	$V_{DS} = 20V, V_{GS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)	I _{GSS}			100	ПА	$VGS = \pm 12V$, $VDS = 0V$
Gate Threshold Voltage	V _{GS(th)}	0.5	_	1.2	V	V _{DS} = V _{GS} , I _D = 250µA
	V GS(III)		20	28		$V_{GS} = 4.5V, I_D = 6.0A$
Static Drain-Source On-Resistance	R _{DS (ON)}		20	20 41	mΩ	$V_{GS} = 2.5V, I_D = 5.2A$
Forward Transfer Admittance	Y _{fs}	_	6	_	S	$V_{DS} = 10V, I_D = 6A$
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_S = 1.7A$
DYNAMIC CHARACTERISTICS (Note 8)	-					
Input Capacitance	C _{iss}	_	550			
Output Capacitance	C _{oss}		88	_	рF	V _{DS} = 10V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss		81		1	
Gate Resistance	Rg		1.34		Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (10V)	Qg		15.6		nC	V _{GS} = 10V, V _{DS} = 10V, I _D = 6A
Total Gate Charge (4.5V)	Qg		7.2			
Gate-Source Charge	Q _{gs}	_	1.0		nC	V_{GS} = 4.5V, V_{DS} = 10V, I_{D} = 6A
Gate-Drain Charge	Q _{gd}		1.9		1	
Turn-On Delay Time	t _{D(on)}		4.69			
Turn-On Rise Time	tr		13.19		1	V _{DD} = 10V, V _{GEN} = 4.5V,
Turn-Off Delay Time	t _{D(off)}		22.10	—	ns	$R_{GEN} = 1\Omega, I_D = 6.7A$
Turn-Off Fall Time	t _f	_	6.43]	

 Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing. Notes:







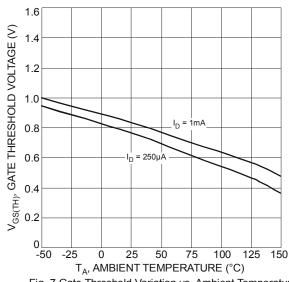
T_A = 25°C

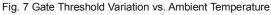
0.8

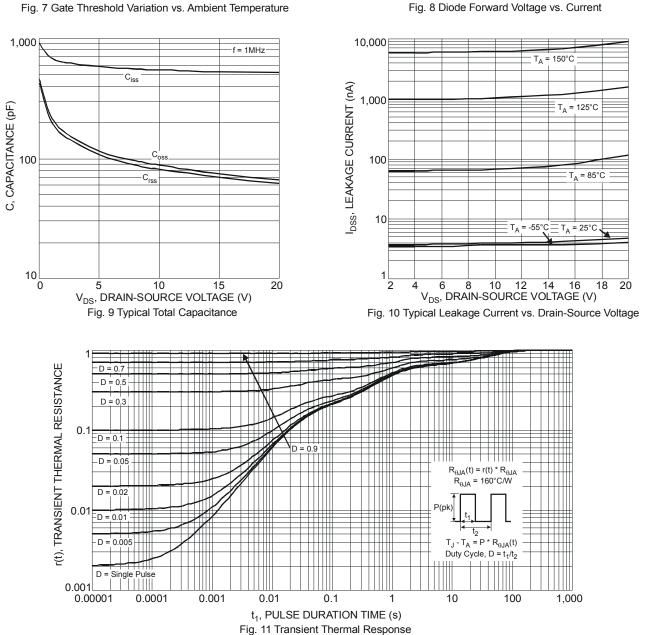
1.0

1.2









20

16

12

8

4

0

0

0.2

0.4

0.6

V_{SD}, SOURCE-DRAIN VOLTAGE (V)

I_S, SOURCE CURRENT (A)

DMN2041L Document number: DS31962 Rev. 2 - 2

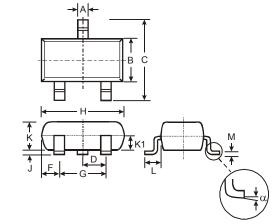
4 of 6 www.diodes.com

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Package Outline Dimensions

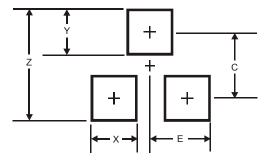
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT-23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
κ	0.903	1.10	1.00				
K1	-	-	0.400				
L	0.45	0.61	0.55				
М	0.085	0.18	0.11				
α	0°	8°	-				
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
C	2.0
E	1.35



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