



20V P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _C = +25°C
	$2.2m\Omega$ @ $V_{GS} = -10V$	-150A
-20V	$2.55 m\Omega$ @ $V_{GS} = -4.5V$	-120A
	4.0mΩ @ V _{GS} = -2.5V	-90A

Description

This new generation MOSFET is designed to minimize $R_{DS(ON)}$ and yet maintain superior switching performance. This device is ideal for use in notebook battery power management and load switch.

Applications

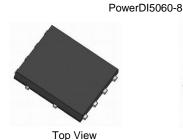
Switch

Features

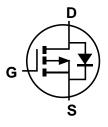
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

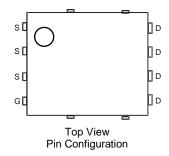
- Case: PowerDI[®] 5060-8
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)







Internal Schematic



Ordering Information (Note 4)

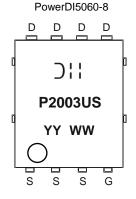
Part Number	Case	Packaging
DMP2003UPS-13	PowerDI5060-8	2,500 / Tape & Reel

Pin1

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ Hanufacturer's Marking
P2003US = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 17 = 2017)
WW = Week Code (01 to 53)

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V_{DSS}	-20	V	
Gate-Source Voltage		V_{GSS}	±12	V
Continuous Drain Current, V _{GS} = -10V (Note 7)	$T_C = +25$ °C $T_C = +70$ °C	I _D	-150 -120	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-350	Α	
Maximum Continuous Body Diode Forward Current (Note 7)	Is	-120	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I _{SM}	-350	Α
Avalanche Current, L = 0.1mH (Note 8)	I _{AS}	-32	Α	
Avalanche Energy, L = 0.1mH (Note 8)		E _{AS}	67	mJ

Thermal Characteristics

Characteristic	-	Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	1.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	90	°C/W
Total Power Dissipation (Note 6)		P _D	2.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	46	°C/W
Total Power Dissipation (Note 7)		P _D	80	W
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	1.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μΑ	$V_{DS} = -16V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)				•			
Gate Threshold Voltage	V _{GS(TH)}	-0.5	_	-1.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
	R _{DS(ON)}	_	1.7	2.2		$V_{GS} = -10V, I_D = -25A$	
Static Drain-Source On-Resistance		_	1.9	2.55	mΩ	$V_{GS} = -4.5V, I_D = -20A$	
	, ,	_	2.5	4.0		$V_{GS} = -2.5V, I_D = -15A$	
Diode Forward Voltage	V _{SD}	_	-0.6	-1.1	V	$V_{GS} = 0V, I_{S} = -5A$	
DYNAMIC CHARACTERISTICS (Note 10)				•			
Input Capacitance	C _{iss}		8352	_	pF		
Output Capacitance	Coss	_	1406	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ - f = 1MHz	
Reverse Transfer Capacitance	C _{rss}	_	599	_	pF		
Gate Resistance	Rg	_	13.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	79	_	nC		
Total Gate Charge (V _{GS} = -10V)	Qq	_	177	_	nC	101/1	
Gate-Source Charge	Q _{gs}	_	14.3	_	nC	$V_{DS} = -10V, I_{D} = -20A$	
Gate-Drain Charge	Q_{gd}	_	19.8	_	nC		
Turn-On Delay Time	t _{D(ON)}		7.8	_	ns		
Turn-On Rise Time	t _R	-	4.9	_	ns	$V_{DD} = -10V$, $V_{GEN} = -4.5V$,	
Turn-Off Delay Time	t _{D(OFF)}		377	_	ns	$R_{GEN} = 1\Omega$, $I_D = -10A$	
Turn-Off Fall Time	t _F	_	189	_	ns	1	
Reverse Recovery Time	t _{RR}	_	49	_	ns	1 100 11/11 1000/	
Reverse Recovery Charge	Q _{RR}	_	39	_	nC	I _F = -10A, di/dt = 100A/μs	

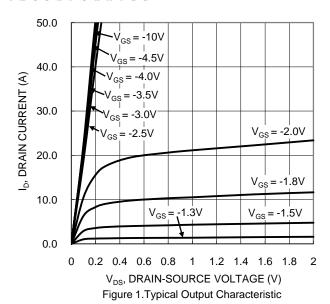
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

 $[\]label{eq:continuous} \textbf{7. Thermal resistance from junction to soldering point (on the exposed drain pad)}.$ 8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.

^{9.} Short duration pulse test used to minimize self-heating effect.

^{10.} Guaranteed by design. Not subject to product testing.





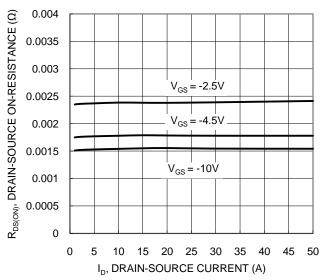


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

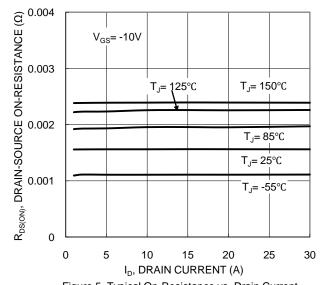
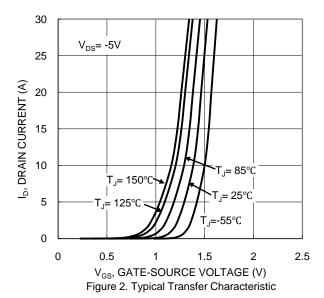


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



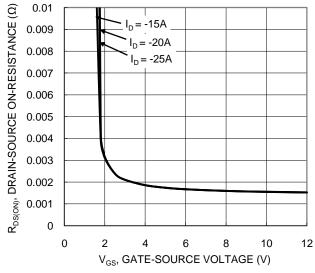


Figure 4. Typical Transfer Characteristic

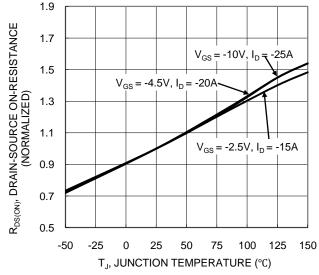


Figure 6. On-Resistance Variation with Junction Temperature





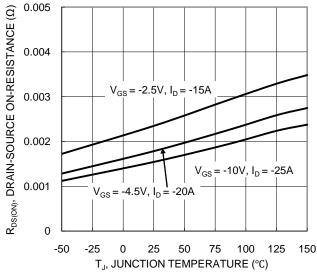
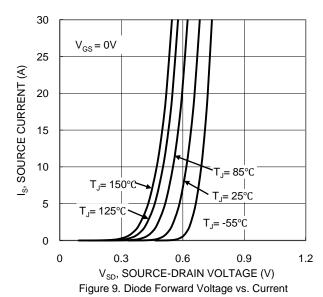
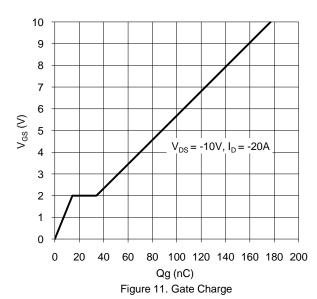


Figure 7. On-Resistance Variation with Junction Temperature





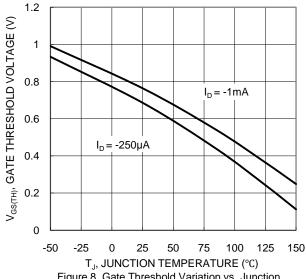
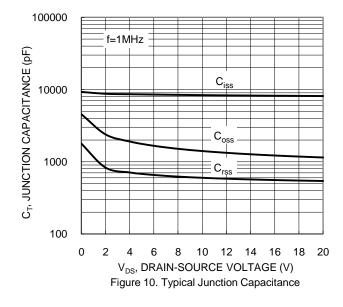
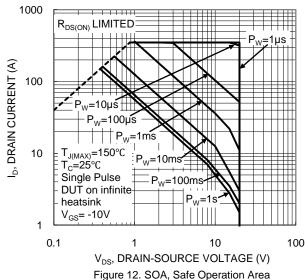


Figure 8. Gate Threshold Variation vs. Junction
Temperature







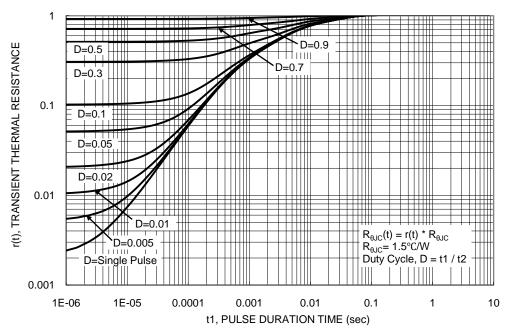


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

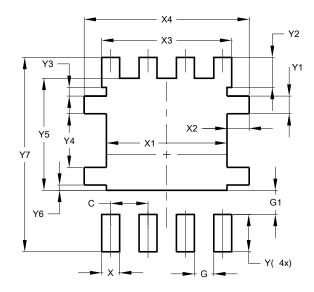
PowerDI5060-8 Detail A Detail A

PowerDI5060-8						
Dim	Min Max Typ					
A	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
C D	5	.15 BS	<u> </u>			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е	6	3.15 BS	0			
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	_	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
M	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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