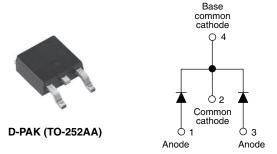


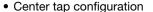
# High Performance Schottky Rectifier, 2 x 3.5 A



PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I <sub>F(AV)</sub>	2 x 3.5 A					
$V_{R}$	40 V					
V <sub>F</sub> at I <sub>F</sub>	See Electrical table					
I <sub>RM</sub>	24 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
Diode variation	Common cathode					
E <sub>AS</sub>	8 mJ					

#### **FEATURES**







- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **DESCRIPTION**

The VS-6CWQ04FNPbF surface mount, center tap, Schottky rectifier series has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS										
SYMBOL	CHARACTERISTICS	VALUES	UNITS							
I <sub>F(AV)</sub>	Rectangular waveform	7	A							
V <sub>RRM</sub>		40	V							
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	500	A							
V <sub>F</sub>	3 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.49	V							
T <sub>J</sub>	Range	-40 to +150	°C							

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-6CWQ04FNPbF	UNITS					
Maximum DC reverse voltage	$V_{R}$	40	V					
Maximum working peak reverse voltage	$V_{RWM}$	40	V					

ABSOLUTE MAXIMUM RATINGS								
PARAMETER		SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average per leg forward current per device		_	50 % duty cycle at T <sub>C</sub> = 135 °C	3.5	A			
		I <sub>F(AV)</sub>	30 % duty cycle at 16 = 103 C	7				
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated		500				
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	80			
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 16 mH		8.0	mJ		
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	Α		



ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONI	VALUES	UNITS				
		3 A	T <sub>.I</sub> = 25 °C	0.53	V			
Maximum forward voltage drop per leg	V <sub>FM</sub> <sup>(1)</sup>	6 A	- IJ=25 C	0.67				
See fig. 1	VFM (1)	3 A	T = 105 °C	0.49				
		6 A T <sub>J</sub> = 125 °C		0.62	1			
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	- V <sub>R</sub> = Rated V <sub>R</sub>	2	- mA			
See fig. 2		T <sub>J</sub> = 125 °C	VR = Nated VR	24				
Threshold voltage	V <sub>F(TO)</sub>	T - T movimum		0.34	V			
Forward slope resistance	r <sub>t</sub>	ıj = ıj maximum	$T_J = T_J$ maximum		mΩ			
Typical junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range	189	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm	5.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

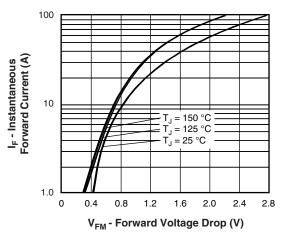
#### Note

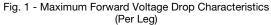
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range		T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-40 to +150	°C		
Maximum thermal resistance,	per leg	D	DC operation	4.70	°C/W		
junction to case	per device	$R_{thJC}$	See fig. 4	2.35	C/ VV		
Approximate weight				0.3	g		
Approximate weight				0.01	OZ.		
Marking device			Case style D-PAK (similar to TO-252AA)	6CWQ	04FN		

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink





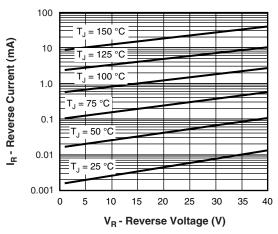


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

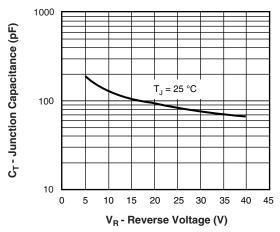


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

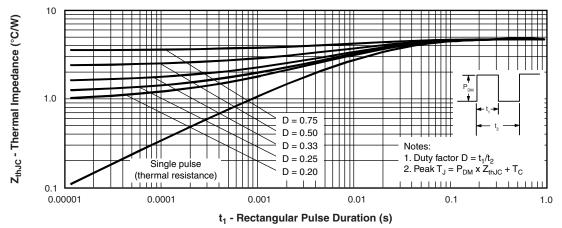


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)



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### Vishay Semiconductors

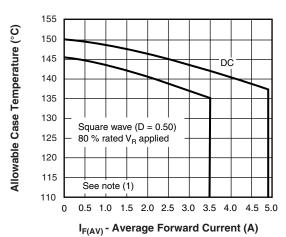
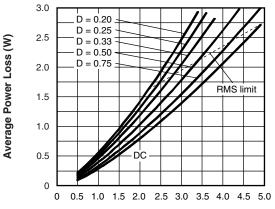
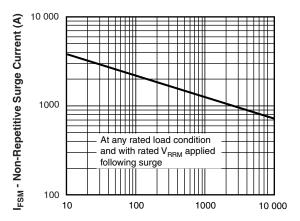


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current (Per Leg)



 $I_{F(AV)}$  - Average Forward Current (A)

Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t<sub>p</sub> - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

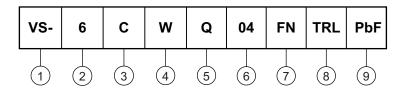
#### Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = 80 \text{ \% rated } V_R \\ \end{array}$ 



### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (7 A)

Center tap configuration

Package identifier:

W = D-PAK

5 - Schottky "Q" series

6 - Voltage rating (04 = 40 V)

7 - FN = TO-252AA (D-PAK)

None = tube (50 pieces)

• TR = tape and reel

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

9 - PbF = lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95016					
Part marking information	www.vishay.com/doc?95059					
Packaging information	www.vishay.com/doc?95033					



**NOTES** 

3

2

MAX.

0.410

0.070

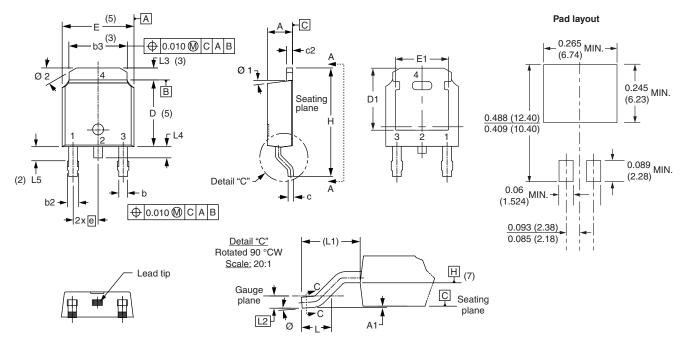
0.050

0.040

0.060

# **D-PAK (TO-252AA)**

#### **DIMENSIONS** in millimeters and inches



Ī	SYMBOL	MILLIM	METERS	INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		
		MIN.	MAX.	MIN.	MAX.	NOTES		STIVIBUL	MIN.	MAX.	MIN.	MAX
ſ	Α	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC
ſ	A1	-	0.13		0.005			Н	9.40	10.41	0.370	0.41
Ī	b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.07
Ī	b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.
ſ	b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC
Ī	С	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.05
Ī	c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.04
ſ	D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.06
Ī	D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°
ſ	Е	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°
Ī	E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°

### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- Lead dimension uncontrolled in L5
- Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- Dimension b1 and c1 applied to base metal only
- (7) Datum A and B to be determined at datum plane H
- Outline conforms to JEDEC outline TO-252AA



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