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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT2287WP

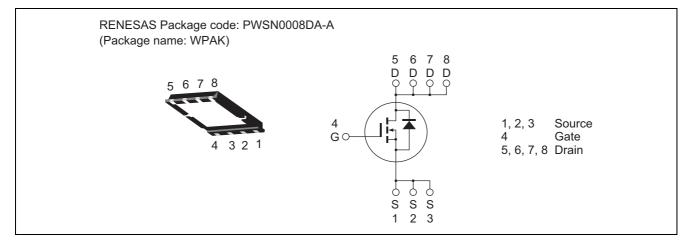
Silicon N Channel Power MOS FET Power Switching

REJ03G1470-0100 Rev.1.00 Sep 06, 2006

Features

- Low on-resistance
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	200	V
Gate to source voltage	V _{GSS}	±30	V
Drain current	I _D	17	А
Drain peak current	Note1 I _{D (pulse)}	34	А
Body-drain diode reverse drain current	I _{DR}	17	А
Body-drain diode reverse drain peak current	Note1 I _{DR (pulse)}	34	А
Avalanche current	I _{AP} ^{Note3}	17	А
Avalanche energy	E _{AR} ^{Note3}	19.2	mJ
Channel dissipation	Pch Note2	30	W
Channel to case thermal impedance	θch-c	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tc = 25°C

3. STch = 25° C, Tch $\leq 150^{\circ}$ C



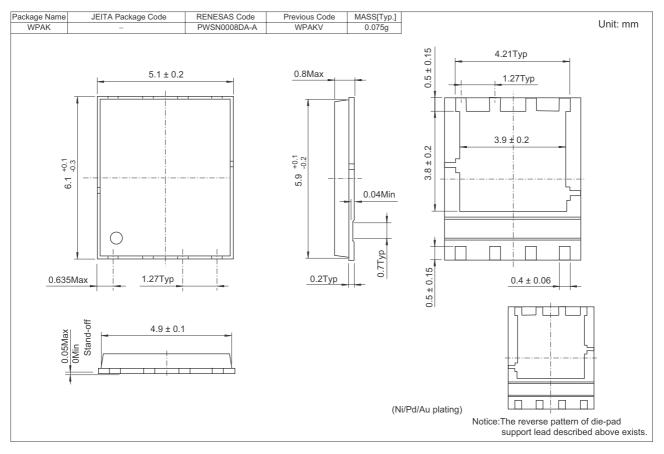
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	V _{(BR)DSS}	200	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	—	—	1	μA	$V_{DS} = 200 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	—	±0.1	μA	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3.0	—	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward transfer admittance	yfs	8	14	_	S	$I_D = 8.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Static drain to source on state	R _{DS(on)}	_	0.084	0.094	Ω	$I_D = 8.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance						
Input capacitance	Ciss	_	1200	—	pF	V _{DS} = 25 V
Output capacitance	Coss	—	220	—	pF	$V_{GS} = 0$ f = 1 MHz
Reverse transfer capacitance	Crss	—	19	_	pF	
Turn-on delay time	t _{d(on)}	—	31	—	ns	$I_{D} = 8.5 \text{ A} \\ V_{GS} = 10 \text{ V} \\ R_{L} = 11.8 \Omega \\ \text{Rg} = 10 \Omega$
Rise time	tr	—	37	—	ns	
Turn-off delay time	t _{d(off)}	_	69	—	ns	
Fall time	t _f	_	8	—	ns	
Total gate charge	Qg	_	26	—	nC	V _{DD} = 160 V
Gate to source charge	Qgs	_	7	_	nC	V _{GS} = 10 V I _D = 17 A
Gate to drain charge	Qgd	_	10	_	nC	
Body-drain diode forward voltage	V _{DF}		0.9	1.4	V	$I_F = 17 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}		130	_	ns	$I_F = 17 \text{ A}, V_{GS} = 0$
						di _F /dt = 100 A/µs

Notes: 4. Pulse test



Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2287WP-EL-E	2500 pcs	Taping

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