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DM74AS245 Octal Bus Transceiver with 3-STATE Outputs

General Description

This advanced Schottky device contains 8 pairs of 3-STATE logic elements configured as octal bus transceivers. These circuits are designed for use in memory, microprocessor systems and in asynchronous bidirectional data buses. Two way communication between buses is controlled by the (DIR) input. Data transmits either from the A bus to the B bus or from the B bus to the A bus. Both the driver and receiver outputs can be disabled via the $\overline{\rm (G)}$ enable input which causes outputs to enter the high impedance mode so that the buses are effectively isolated.

Features

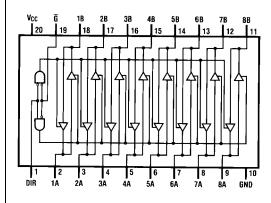
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Non-inverting logic output
- 3-STATE outputs independently controlled on A and B buses
- \blacksquare Low output impedance to drive terminated transmission lines to 133 Ω
- \blacksquare Switching response specified into 500 Ω /50 pF
- Specified to interface with CMOS at $V_{OH} = V_{CC} 2V$
- PNP inputs reduce input loading
- Switching specifications guaranteed over full temperature and V_{CC} range

Ordering Code:

Order Number	Package Number	Package Description
DM74AS245WM	M20B	20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide
DM74AS245SJ	M20D	20-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
DM74AS245N	N20A	20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Con Inp	itrol uts	Operation
G	DIR	
L	L	B Data to A Bus
L	Н	A Data to B Bus
Н	Х	Hi-Z

Absolute Maximum Ratings(Note 1)

Supply Voltage, V_{CC} 7V

Input Voltage

Control Inputs 7V I/O Ports 5.5V

Operating Free Air Temperature Range 0° C to 70° C Storage Temperature Range -65° C to $+150^{\circ}$ C

Typical θ_{JA}

N Package 51.5°C/W
M Package 76.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Тур	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-15	mA
I _{OL}	LOW Level Output Current			48	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

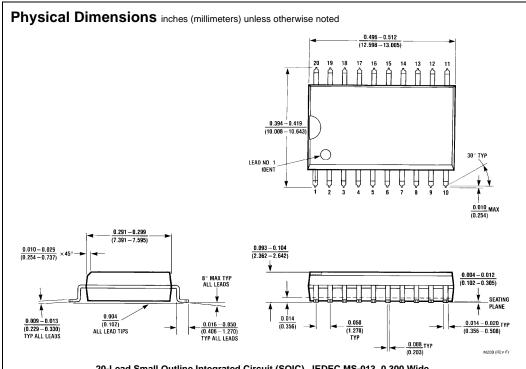
over recommended operating free air temperature range

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V$, $I_{IN} = -18 \text{ mA}$				-1.2	V
V _{OH}	HIGH Level Output	$V_{CC} = 4.5V$, $I_{OH} = -3$ mA		2.4	3.2		
	Voltage	$V_{CC} = 4.5V$, $I_{OH} = -15 \text{ mA}$		2	2.3		V
		$I_{OH} = -2 \text{ mA}, V_{CC} = 4.5 \text{V to } 5.5 \text{V}$		V _{CC} – 2			
V _{OL}	LOW Level Output Voltage	$V_{CC} = 4.5V$, $I_{OL} = Max$			0.35	0.55	V
I _I	Input Current at Max	$V_{CC} = 5.5V, V_{IN} = 7V,$				0.1 mA	
	Input Voltage	(V _{IN} = 5.5V for A or B Ports)				0.1	IIIA
I _{IH}	HIGH Level Input Current	V _{CC} = 5.5V,	Control Inputs			20	μА
		$V_{IN} = 2.7V$	A or B Ports			70	μΑ
I _{IL}	LOW Level Input Current	V _{CC} = 5.5V,	Control Inputs			-0.5	mA
		V _{IN} = 0.4V	A or B Ports			-0.75	IIIA
Io	Output Drive Current	V _{CC} = 5.5V, V _{OUT} = 2.25V	•	-50		-150	mA
I _{CC}	Supply Current	V _{CC} = 5.5V	Output HIGH		62	97	
			Output LOW		95	149	mA
			3-STATE		79	123	

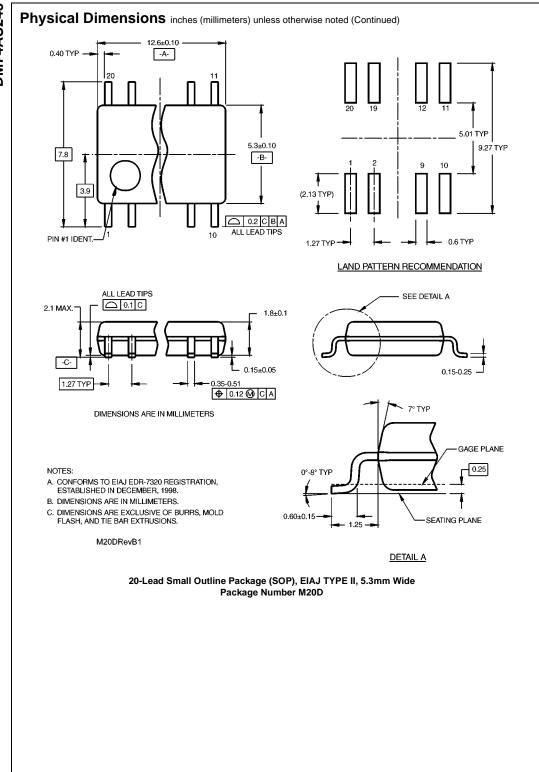
Switching Characteristics

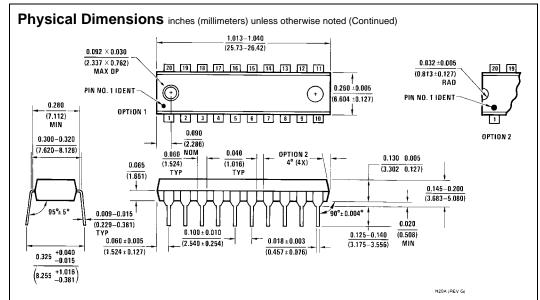
over recommended operating free air temperature range

Symbol	Parameter	Conditions	From	То	Min	Max	Units
t _{PLH}	Propagation Delay Time HIGH-to-LOW Level Output	$V_{CC} = 4.5V$ to 5.5V, $R_1 = R_2 = 500\Omega$,	A or B	B or A	2	7.5	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	C _L = 50 pF	A or B	B or A	2	7	ns
t _{PZL}	Output Enable Time to LOW Level		G	A or B	2	8.5	ns
t _{PZH}	Output Enable Time to HIGH Level		G	A or B	2	9	ns
t _{PLZ}	Output Disable Time from LOW Level		G	A or B	2	9.5	ns
t _{PHZ}	Output Disable Time from HIGH Level		G	A or B	2	5.5	ns



20-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-013, 0.300 Wide Package Number M20B





20-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N20A

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